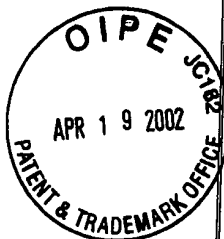


04/22/02

2836 #2
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: James Holt; John So
 Assignee: Fairchild Semiconductor Corporation
 Title: Over-Voltage Protection Circuit
 Application No.: 10/027,708 Filing Date: December 19, 2001
 Examiner: Unknown Group Art Unit: Unknown
 Docket No.: M-11811 US



San Francisco, California
 April 19, 2002

COMMISSIONER FOR PATENTS
 Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT
 UNDER 37 CFR § 1.97(b)

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying form PTO-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are enclosed.

Citation of these documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

EXPRESS MAIL LABEL NO:

EV 029 355 115 US

Respectfully submitted,

Philip W. Woo
 Philip W. Woo
 Attorney for Applicants
 Reg. No. 39,880

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U.S. Department of Commerce, Patent and Trademark Office					Atty Docket No.		Serial No.	
					M-11811 US		10/027708	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Applicant(s)			
(Use several sheets if necessary)					James Holt, John So			
					Filing Date		Group	
					December 19, 2001		Unknown	
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
GL	CA	4,573,099	2/25/1986	Ganesan et al.	361	56		
GL	CB	6,100,677	8/8/2000	Farrenkopf	323	285		
GL	CC	5,381,061	1/10/1995	Davis	326	57		
GL	CD	5,319,259	6/7/1994	Merrill	307	443		
GL	CE	5,063,304	11/5/1991	Iyengar	307	296.6		
GL	CF	5,161,183	11/3/1992	Maytum	379	412		
GL	CG	5,479,119	12/26/1995	Tice et al.	327	74		
GL	CH	5,815,359	9/29/1998	Maytum et al.	361	111		
GL	CI	5,581,170	12/3/1996	Mammano et al.	320	17		
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	CJ							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
GL	CK	Analog Devices, "IMVP-II-Compliant Core Power Controller for Mobile CPUs," 2001, ADP3422.						
GL	CL	Maxim, "Notebook CPU Step-Down Controller for Intel Mobile Voltage Positioning (IMVP-II)," www.maxim-ic.com , February, 2001, MAX1718.						
GL	CM	Semtech, "Portable IMVP-IV™ Dual Phase Power Supply Controller," September, 2001, SC1476.						
GL	CN	Texas Instruments, "High-Density Synchronous Buck Converter Design Using TPS56xx Controllers", User's Guide, June 1999.						
GL	CO	Harris Corporation, "Advanced PWM and Dual Linear Power Control with Integrated ACPI Support Interface," December 1998, HIP6028.						
GL	CP	Di Bernardo et al., "Grazing, skipping and sliding: analysis of the non-smooth dynamics of the DC/DC buck converter," September 22, 1997, journal of publication unknown.						
Examiner		Date Considered						
Gary L. Laxton		6-27-03						
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.								

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					December 19, 2001		Unknown	
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA	5,091,818	2/25/1992	Morikawa et al.	361	56		
	AB	5,654,858	8/5/1997	Martin et al.	361	56		
	AC	5,784,231	7/21/1998	Majid et al.	361	18		
	AD	5,781,390	7/14/1998	Notaro et al.	361	84		
	AE	5,963,407	10/5/1999	Fragapane et al.	361	42		
	AF	5,818,120	10/6/1998	Palara	307	10.6		
	AG	5,285,344	2/8/1994	Heitzmann	361	22		
	AH	5,088,018	2/11/1992	Lee	363	56		
	AI	5,818,670	10/6/1998	Ahn	361	18		
	AJ	5,771,162	6/23/1998	Kwon	363	56		
	AK	5,764,467	6/9/1998	Seo	361	91		
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	AQ	Intersil, "Microprocessor CORE Voltage Regulator Multi-Phase Buck PWM Controller," 3/2000, file 4765.1.						
	AR	National Semiconductor, "LM2633, Advanced Two-Phase Synchronous Triple Regulator Controller for Notebook," www.national.com/pf/LM/LM2633.html , 2002.						
	AS	Linear Technology, "Dual High Efficiency, Low Noise, Synchronous Step-Down Switching Regulators," 1996, LTC 1538-AUX/LTC 1539.						
Examiner <i>Gary L. Lorton</i>		Date Considered <i>6-27-03</i>						
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U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
CE	BA	5,751,531	5/12/1998	Rault	361	56		
CE	BB	5,930,096	7/27/1999	Kim	361	91		
CE	BC	6,052,022	4/18/2000	Lee	327	589		
CE	BD	5,130,883	7/14/1992	Edwards	361	91		
CE	BE	5,111,353	5/5/1992	Kotowski et al.	361	91		
CE	BF	5,359,281	10/25/1994	Barrow et al.	323	284		
CE	BG	6,061,218	5/9/2000	Ida et al.	361	56		
CE	BH	4,879,625	11/7/1989	Potenzzone	361	90		
CE	BI	5,164,874	11/17/1992	Okano et al.	361	56		
CE	BJ	5,483,406	1/9/1996	Bennett et al.	361	56		
CE	BK	5,335,132	8/2/1994	DeShazo, Jr.	361	18		
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	BL							
	BM							
	BN							
	BO							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
CE	BP	Linear Technology, "Wide Operating Range, No Rsense™ Step-Down Contoller," 2000 LTC1778/LTC1778-1.						
CE	BQ	Analog Devices, "5-Bit Programmable 2-Phase Synchronous Buck Controller," 2001, ADP3160.						
CE	BR	Analog Devices, "5-Bit Programmable Dual Power Supply Controller for Pentium® III Processors," 1999, ADP3154.						
Examiner		Date Considered						
Cary L. Carbin		6-27-03						
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